

<u>L28</u>	L24 and I8	6	<u>L28</u>
<u>L27</u>	L24 and I7	0	<u>L27</u>
<u>L26</u>	L24 and I6	23	<u>L26</u>
<u>L25</u>	L24 and I5	89	<u>L25</u>
<u>L24</u>	(block\$1 or portion\$1 or bucket\$1 or chunk\$1 or segment\$1) and I4	384	<u>L24</u>
<i>DB=PGPB; PLUR=YES; OP=OR</i>			
<u>L23</u>	(general near1 purpose near1 register\$1 and (transfer\$5 or send\$3 or sent) and (indicat\$3 or specif\$6) and offset\$1 and (portion\$1 or block\$1) and element\$1 and (count or number)).clm.	1	<u>L23</u>
<i>DB=PGPB,USPT,USOC; PLUR=YES; OP=OR</i>			
<u>L22</u>	L21 and I2	5	<u>L22</u>
<u>L21</u>	(moyer near1 william).in.	234	<u>L21</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L20</u>	I2 and I8	46	<u>L20</u>
<u>L19</u>	I2 and I7	6	<u>L19</u>
<u>L18</u>	I2 and I6	57	<u>L18</u>
<u>L17</u>	I2 and I5	217	<u>L17</u>
<u>L16</u>	I4 and I8	6	<u>L16</u>
<u>L15</u>	I4 and I7	0	<u>L15</u>
<u>L14</u>	I4 and I6	23	<u>L14</u>
<u>L13</u>	I4 and I5	89	<u>L13</u>
<u>L12</u>	I3 and I8	6	<u>L12</u>
<u>L11</u>	I3 and I7	0	<u>L11</u>
<u>L10</u>	I3 and I6	23	<u>L10</u>
<u>L9</u>	I3 and I5	89	<u>L9</u>
<u>L8</u>	(710/52-60)[CCLS]	3592	<u>L8</u>
<u>L7</u>	(711/170-173)[CCLS]	4105	<u>L7</u>
<u>L6</u>	(712/2-25, 225-228)[CCLS]	3010	<u>L6</u>
<u>L5</u>	(712/2-300)[CCLS]	13690	<u>L5</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L4</u>	L3 and (length or size or width)	389	<u>L4</u>
<u>L3</u>	L2 and (offset)	406	<u>L3</u>

<u>L2</u>	(transmi\$5 or transfer\$5 or sent or send\$3 or mov\$3) near12 (indicat\$4 or assign\$5 or specif\$5 or designat\$4) near12 (number of count\$3) near12 (stor\$6 or memor\$4 or ram or sram or dram) near12 register\$1	2014	<u>L2</u>
<u>L1</u>	(transmi\$5 or transfer\$5 or sent or send\$3 or mov\$3) near12 (indicat\$4 or assign\$5 or specif\$5 or designat\$4) near12 (number of count\$3)	110471	<u>L1</u>

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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Procedural level address offset assignment of DSP applications with loops**
 Zhang, Y.; Yang, J.;
[Parallel Processing, 2003. Proceedings, 2003 International Conference on](#)
 2003 Page(s):21 - 28
 Digital Object Identifier 10.1109/ICPP.2003.1240562
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- ☐ 2. **A uniform optimization technique for offset assignment problems**
 Leupers, R.; David, F.;
[System Synthesis, 1998. Proceedings, 11th International Symposium on](#)
 2-4 Dec. 1998 Page(s):3 - 8
 Digital Object Identifier 10.1109/ISSS.1998.730589
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L30 I24 and I23

1. L30

L29 I24 and I21

5 L29